Design and draw the circuit diagram of a Mod-6 ripple counter.

Answer:
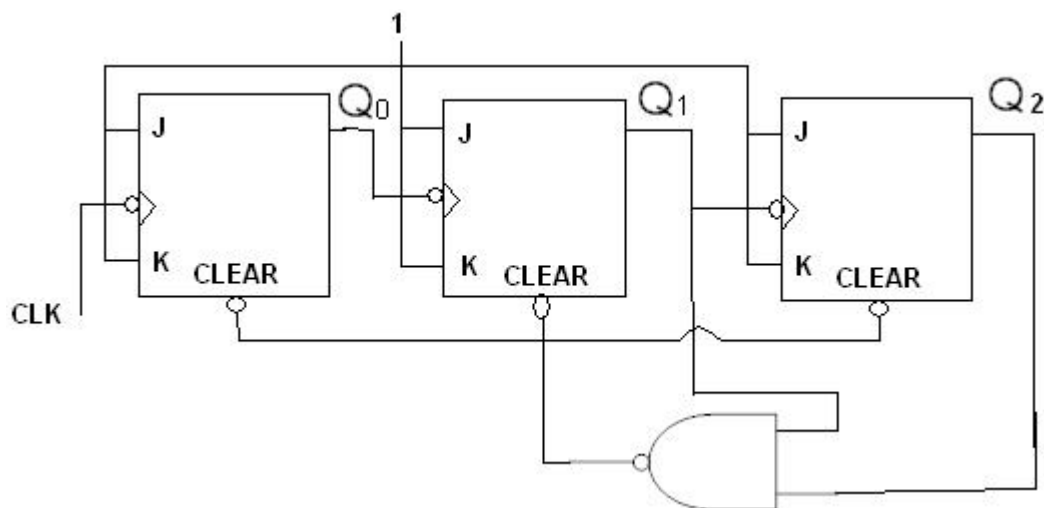
Q2	Q1	Q0	Output
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

A counter is a register that goes through a predetermined sequence of states upon the application of clock pulses.
Asynchronous Counters (or Ripple counters):
- the clock signal (CLK) is only used to clock the first FF (Flip Flop)
- each FF (except the first FF) is clocked by the preceding FF
Modulus (MOD) – the number of states it counts in a complete cycle before it goes back to the initial state.

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